

## MunEDA Technical Forum - Taiwan 2008

We cordially invite you to meet the industrial experts on circuit design-for-yield at MunEDA Technical Forum Taiwan. We are pleased to present excellent speakers & contributions.

**Considering** the effects of process variation, mismatch and operating conditions on the performance and yield of circuits becomes more and more important for high-performance circuit designs. MunEDA software tools for analysis and optimization for circuit design-for-yield (DFY)

help circuit designers worldwide to significantly speed up their design task and to improve performance, robustness and yield of the resulting analog/mixed-signal/RF designs, as well as full-custom digital circuits and digital library elements.

### Topics of MunEDA Technical Forum - Taiwan 2008

- **Circuit Design for Yield with MunEDA WiCkeD™**  
Interactive and automatic software for analysis and optimization circuit performance and yield on transistor level. Technical background presentation and software demonstration.
- **Industrial Application Cases: Analog Design Synthesis Platform (Faraday Technology Corporation)**
- **Industrial Application Cases for Circuit Analysis and Optimization (e.g. from STMicroelectronics, Infineon, austriamicrosystems, ZMD, Bosch Semiconductor, IMMS, X-Fab, and others)**
  - **Analog & Mixed-Signal Design:** OpAmps, Bandgaps, PLL, VCO, Powerlogic, RF circuits,...
  - **Memory Design:** SRAM, DRAM, Sense Amplifiers, ...
  - **Library Cell Design:** Full Custom Macros, Digital Cells, ...

## AGENDA - Friday, April 18th, 2008

08:45 h - 09:10 h	<b>Registration &amp; Welcome Coffee</b>
09:10 h - 09:15 h	<b>Welcome &amp; Introduction</b> Chin-Lung King (金清隆), GTI Grand Technology, Inc.
09:15 h - 09:45 h	<b>Robust Design Methodologies for Deep Submicron Technologies</b> Prof. Dr. Mi-Chang Chang (張彌彰), National Tsing Hua University
09:45 h - 10:15 h	<b>Circuit Design for Yield with MunEDA WiCkeD</b> Michael Pronath, MunEDA GmbH
10:15 h - 10:30 h	<b>Coffee &amp; Discussion Break</b>
10:30 h - 11:00 h	<b>WiCkeD Software Demonstration</b> Simon Hong (洪瑞華), GTI Grand Technology, Inc.
11:00 h - 11:30 h	<b>FARADAY: Analog Design Synthesis Platform with WiCkeD</b> David Chang-Chung Wu (吳章昌), Faraday Technology Corporation
11:30 h - 12:00 h	<b>Industrial WiCkeD DFM-DFY Application Cases</b> Michael Pronath, MunEDA GmbH
12:00 h - 12:15 h	<b>General Discussion, Q&amp;A</b> May Chen (陳巧美), GTI Grand Technology, Inc.
12.20 h	<b>Lunch &amp; Talks</b>

### Intended Audience:

Circuit Designer Analog Mixed-Signal / Memory, Digital and Analog Cell Libraries, Design Manager, CAD Engineer, CAD Manager, Design Methodologists, CTO, ...

### With contributions from



Register at <http://www.muneda.com/MUGM/Taiwan-2008/Registration>  
or send email to: [maychen@grandti.com.tw](mailto:maychen@grandti.com.tw) or [info@muneda.com](mailto:info@muneda.com)

## MunEDA Technical Forum Taiwan 2008

### MTFT2008 - Speakers



**Prof. Mi-Chang Chang (張彌彰), National Tsing Hua University**

Dr. Chang received his Ph.D. from University of Illinois, USA. He was with TSMC for library and IP development from 1999 to 2007. During this time, he led the teams in developing robust circuit blocks that form the basis for IC designs in advanced technologies. Before TSMC, he was with Texas Instruments RD organization and participated in IC design, technology development as well CAD development. His interests include circuit and system design, circuit and technology cooptimization, and CAD for circuit and device developments. Dr. Chang can be reached by email, michang@ee.nthu.edu.tw.



**David Wu (吳章昌), R&D Technical Director, Faraday Technology Corporation**

Faraday Technology Corporation is a leading silicon IP and fabless ASIC vendor. The company's broad silicon IP portfolio includes Cell Library, Memory Compiler, ARM-compliant CPUs, DDRII, MPEG4, H.264, USB 2.0, 10/100 Ethernet, Serial ATA, PCI Express, and UWB. With 2007 revenue of US\$ 156 million, Faraday is one of the largest fabless ASIC companies in the Asia-Pacific region, and it also has a significant presence in other world-wide markets. Headquartered in Taiwan, Faraday has service and support offices around the world, including the U.S., Japan, Europe, and China. For more information, please visit: www.faraday-tech.com



**Simon Hong (洪瑞華), Applications Engineer, GTI Grand Technology Inc.**

Founded in 1996, Grand Technology Inc. (GTI) is a leading pure application-specific software representative with a clear focus on providing the best available tools for the development of future chip and micro system technologies. Based on this, GTI provides application-specific software and hardware, consulting services, customer specific support as well as education and training for the semiconductor industry. Grand Technology Inc. keeps very good relationship with all customers in the semiconductor companies in Taiwan. For more information, please visit the GTI web site at www.grandti.com.tw.



**Dr. Michael Pronath, Vice President Products & Solutions, MunEDA GmbH**

Dr. Michael Pronath serves as MunEDA's Vice President Products & Solutions since co-founding the company in 2001. Prior to founding MunEDA, he received his Ph.D. from the Institute for Electronic Design Automation at the Technical University of Munich. He has extensive experience in statistical analysis and modeling, and optimization. Dr. Pronath is author and coauthor of several international publications about methods of analog integrated circuit design and testing of mixed-signal circuits. Contact at www.muneda.com.

#### MunEDA WiCkeD™ DFM-DFY Tool Overview

WiCkeD™ is a comprehensive and powerful software tool suite for interactive, manual, semi- and fully automatic performance analysis, modelling, sizing, design centering and yield optimization of analog, mixed-signal and digital circuits. WiCkeD™ can be used either via fully graphical GUI Interface as well as via Scripting Interface in batch mode.

WiCkeD™ contains interactive and fully automatic tools and methodologies for

- > Topology Analysis & Optimization
- > Nominal Performance Analysis & Optimization
- > Statistical Yield Analysis & Optimization
- > Response Surface Modelling

WiCkeD™ is fully integrated in Cadence, Mentor and Synopsys Analog Design Environments



**Date:** Friday, April 18th, 2008  
**Location:** The Ambassador Hotel Hsinchu  
 188, Chung Hwa Road, Sec 2,  
 Hsinchu 300, Taiwan R.O.C.  
**Phone:** +886 3 515 1111



Participation of MunEDA Technical Forum Taiwan 2008 is free but early registration is highly recommended. We are very looking forward to welcome you to our MunEDA Technical Forum 2008 in Hsinchu/Taiwan.

**May Chen (陳巧美)**  
 Grand Technology, Inc.  
 盟佳科技股份有限公司  
 Phone: +886 3 56 22 675

**AND  
 YOUR  
 TEAM**

**Dr. Michael Pronath**  
 Vice President Products & Solutions  
 MunEDA GmbH/Inc.

**Register at <http://www.muneda.com/MUGM/Taiwan-2008/Registration>  
 or send email to: [maychen@grandti.com.tw](mailto:maychen@grandti.com.tw)**