

Program Announcement – MunEDA MTF08 Boston

We cordially invite you to meet industrial experts on circuit design-for-yield at MunEDA Technical Forum 2008 for the Boston Area. We are pleased to present excellent speakers & contributions for the topic:

DESIGN FOR MANUFACTURABILITY AND YIELD DFM-DFY IN ANALOG AND MIXED-SIGNAL CIRCUIT DESIGN

Considering the effects of process variation, mismatch and operating conditions on the performance and yield of circuits becomes more and more important for high-performance circuit designs. MunEDA software tools for analysis and optimization for circuit design-for-yield (DFY)

help circuit designers worldwide to significantly speed up their design task and to improve performance, robustness and yield of the resulting analog/mixed-signal/RF designs, as well as full-custom digital circuits and digital library elements.



Date: Tuesday, June 17th 2008
Location: Trilogic Inc. (www.trilogic.com)
10 New England Business Center
Andover, MA 01810, USA



09.00 h - 09.30 h am	Registration & Welcome Coffee
SESSION PART I	WiCkeD DFM-DFY METHODOLOGY & APPLICATION PRESENTATIONS
09.30 h - 09.45 h am	Welcome & What's new Chuck Donovan, Vice President Sales, Trilogic Inc., Andover, MA
09.45 h - 10.30 h am	Introduction into Front-End Design for Manufacturability & Yield with MunEDA's WiCkeD DFM-DFY design analysis and optimization tools Michael Pronath, MunEDA
10:30 h – 11:00 h am	ST Microelectronics: Industrial Designflow Integration and Application Cases with WiCkeD DFM-DFY Tools in STMicroelectronics UNICAD Design Environment Pierluigi Daglio, STMicroelectronics, Agrate, Italy
11.00 h - 11.15 h am	Coffee Break
SESSION PART II	WiCkeD SOFTWARE & DESIGNFLOW INTEGRATION TUTORIALS
11.15 h - 11.45 h am	WiCkeD 6.0 – Software Tutorial & Release Updates Ernie Frohring, Trilogic, Volker Glöckel, MunEDA
11.45 h – 12:15 h pm	ON Semiconductor – Optimization of Oscillator Design and Error Amplifier with WiCkeD David J. Cretella, ON Semiconductor, East Greenwich, RI
12.15 h - 12.30 h pm	Special Topic - Integration of WiCkeD into Mentor Graphics Custom IC Flow with IC Studio and Eldo® Volker Glöckel, MunEDA
12.30 h - 12.45 h pm	Special Topic - Automatic Flow for Library Optimization based on Synopsys HSpice® Michael Pronath, MunEDA
12.45 h - 01.00 h pm	Wrap up and Q&A Session Chuck Donovan, Vice President Sales, Trilogic Inc., Andover, MA, USA
From 01.00 h pm	Lunch & Farewell Coffee
AFTER LUNCH SESSION	Optional Discussion about Tools and Methodologies with MunEDA and Trilogic experts

Register at: http://www.muneda.com/MUGM-MTF_Boston-2008_Registration
or send email to info@trilogic.com

We are very pleased to invite you to our MunEDA Technical Forum Boston 2008 in Andover.



Andreas Ripp
VP Sales & Marketing
MunEDA GmbH/Inc.
www.muneda.com

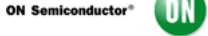


Chuck Donovan
Vice President Sales
Trilogic Inc.
www.trilogic.com

Participation for MTF Boston 2008 is free but early registration is highly recommended.

MunEDA Technical Forum 2008 – Boston Area

MTF 2008 Boston Area - Speakers



David J. Cretella, Senior Design Engineer, Automotive Business Unit, ON Semiconductor, East Greenwich, RI
 ON Semiconductor (Nasdaq: ONNN) is a worldwide leader in analog and mixed signal technology and design. With its global logistics network and strong product portfolio, ON Semiconductor is a preferred supplier of efficient power solutions to customers in the power supply, automotive, communication, computer, digital and consumer, medical, industrial, and military/aerospace markets. The company's broad portfolio includes power, analog, DSP, mixed-signal, advance logic, clock management and standard component devices. Global corporate headquarters are located in Phoenix, Arizona, USA. The company operates a network of manufacturing facilities, sales offices and design centers in key markets throughout North America, Europe, and the Asia Pacific regions. For more information please visit ON Semiconductor at www.onsemi.com.



Pierluigi Daglio, AMS Flows & Methods Manager NVM Design Platform, FTM Central CAD & Design Solutions, STMicroelectronics, Agrate, Italy
 STMicroelectronics is the world fifth's largest semiconductor company with market leadership in many fields and net revenues of US\$9.85 billion in 2006 and US\$4.69 billion for the first half of 2007. The Company's sales are well balanced between the semiconductor industry's five major high-growth sectors (percentage of ST's sales in 2007): Communications (35%), Consumer (17%), Computer (16%), Automotive (16%) and Industrial (16%). Visit www.st.com.



Dr. Michael Pronath, MunEDA
 Michael Pronath is MunEDA Vice President Products & Solutions. He holds a PhD in Electrical Engineering from the Technical University of Munich as well as a MBA of university of Hagen. Dr. Pronath is author and coauthor of several international publications about methods of analog integrated circuit design and testing of mixed-signal circuits.



Ernie Frohring, Senior Applications Engineer, Trilogic Inc.
 For more than 15 years, TRILOGIC has provided software and services enabling the North East's engineering community to design, develop and deliver products. Trilogic sells software solutions from industry leaders such as Mentor Graphics and Omnify, and also provide support, training, and consultation services to help achieve customer's success sooner. Product ranges board level/eng design, PCB design, Hybrid/Package Design, System Level Design, Simulation & Verification, SI Models, EMI/EMC Analysis, FPGA/ASIC Analysis & Design, Design Data Management, IC Design Analysis & Optimization and more. Trilogic is located in Andover, Massachusetts, USA.



Dr. Volker Glöckel, MunEDA
 Volker Glöckel is Senior Technical Account Manager with MunEDA and has published several papers about automated design and test analysis and optimization methods. He holds a Diploma and a PhD in Electrical Engineering from TUM.

MunEDA WiCkeD™ DFM-DYF Tool Overview

WiCkeD™ is a comprehensive and powerful software tool suite for interactive, manual, semi- and fully automatic performance analysis, modelling, sizing, design centering and yield optimization of analog, mixed-signal and digital circuits. WiCkeD™ can be used either via fully graphical GUI Interface as well as via Scripting Interface in batch mode.

WiCkeD™ contains interactive and fully automatic tools and methodologies for

- > Topology Analysis & Optimization
- > Nominal Performance Analysis & Optimization
- > Statistical Yield Analysis & Optimization
- > Response Surface Modelling

WiCkeD™ is fully integrated in Cadence, Mentor and Synopsys Analog Design Environments



With friendly support from:



ON Semiconductor®



Visit MunEDA and Trilogic also at
**MunEDA's booth 2629 in Hall D at
 DAC2008 June 09-12 in Anaheim
 Convention Center,
 Anaheim, CA, USA**
www.dac.com



Register at: http://www.muneda.com/MUGM-MTF_Boston-2008_Registration
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