

MunEDA

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MunEDA WiCkeD™: Improve Design Performance and Yield

MunEDA provides leading EDA technology for analysis and optimization of yield and performance of analog, mixed-signal and custom digital designs. MunEDA's products and solutions enable customers to reduce the design time of their circuits and to maximize robustness and yield. MunEDA's tools are in industrial use in the areas of communication, computer, memories, automotive, and consumer electronics by leading semiconductor companies worldwide and proven in more than 1000+ design and tape-out projects.

MunEDA was founded in 2001 and is a privately held company. The company headquarter is located in Munich, Germany. MunEDA has offices in Munich, Germany (Headquarter) and Sunnyvale, California, USA (MunEDA Inc.).

MunEDA is represented by leading EDA distribution companies worldwide in USA, Japan, Korea, Taiwan, Singapore, Malaysia, Scandinavia, and others (see the Contact section on [www.muneda.com](http://www.muneda.com)).

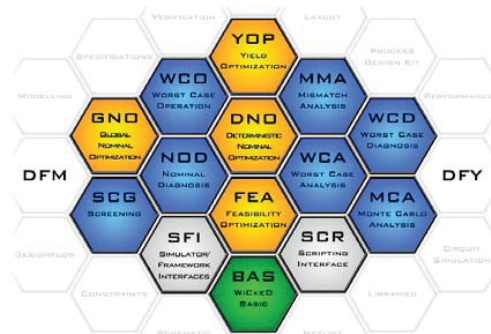


WiCkeD™ DFM-DFY Tool Overview

WiCkeD™ is a comprehensive and powerful interactive software tool suite for manual, semi- and fully automatic analysis and optimization of performance, robustness and yield as well as performance modelling of analog and mixed-signal circuits.

WiCkeD™ includes tools and methodologies for

- Topology Analysis & Optimization
- Nominal Performance Analysis & Optimization
- Yield Analysis & Optimization
- Response Surface Modelling



Picture: MunEDA WiCkeD™ Tools Overview

WiCkeD™ can be operated either through a graphical user interface or through a programmable scripting interface (batch mode).

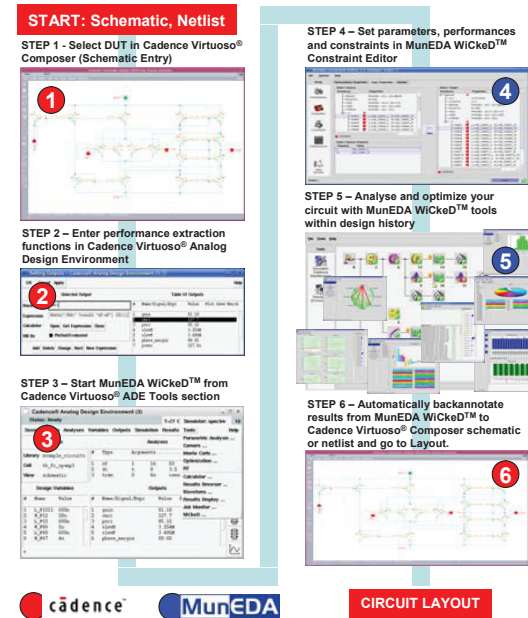
WiCkeD™ 6-step circuit design performance and yield analysis and optimization starting from Cadence Virtuoso®

A user of WiCkeD™ typically takes the following 6 steps to analyze/optimize a circuit starting from Cadence Virtuoso® Composer and Analog Design Environment:

- STEP 1 -- Select DUT in Cadence Virtuoso® Composer (schematic entry).
- STEP 2 -- Define outputs in Cadence Virtuoso® Analog Design Environment.

- STEP 3 -- Start MunEDA WiCkeD™ from Cadence Virtuoso® ADE Tools menu.
- STEP 4 -- Set parameters, performances (outputs) and constraints in MunEDA WiCkeD™ Constraint Editor.
- STEP 5 -- Analyze and optimize your circuit with MunEDA WiCkeD™.
- STEP 6 -- Automatically backannotate results from MunEDA WiCkeD™ to Cadence Virtuoso® Composer and continue with layout generation.

This 6-step circuit design performance and yield analysis and optimization approach is also described in the following picture.



Picture: 6-Step Design Analysis & Optimization Flow with Cadence Virtuoso® and MunEDA WiCkeD™ Tools

WiCkeD™ - Seamless integration into Cadence Virtuoso®

WiCkeD™ is seamlessly integrated into Cadence Virtuoso® Analog Design Environment supporting all major standard industrial circuit simulators like Spectre/SpectreRF®, Eldo® and HSpice®. It can be started directly from the Tools menu of Cadence Virtuoso® Analog Design Environment with fully automated annotation/backannotation from/to schematic and/or netlist.

MunEDA is member of the Cadence Connections Partner Program since 2004.



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